



Technical Announcement
IABG, DELTA ELECTRONICS, INC

Product	DVP PLC	Applicable Model	EH3 EH3-L SV2	Confidentiality Level	<input checked="" type="checkbox"/> General <input type="checkbox"/> Classified <input type="checkbox"/> Top secret
				ECN No.	N/A
Published by	PLC BU	Publisher	Andy Due	Publication No.	134A-P-EH3EH3LSV2-TAN02-EN
				Date of Publication	Jan. 05, 2016
Published to	Taiwan Sales, Product Manager, DGC, DEU, DPR, DES				

Object: The firmware of DVP-EH3/DVP-EH3-L is updated (Version 2.00→Version 2.04) and the firmware of DVP-SV2 is also updated (Version 2.02→Version 2.04).

Purpose: The functions which are modified, and the functions which are added are described.

Description:

《New Functions and Instructions》

1. PLSV, the Adjustable Speed Pulse Output instruction, has added M1616 which gives high priority to the positioning. When instructed to change the direction of the speed, M1616 will control the timing to change.
 - When M1616 is set to OFF (by default), the instruction of changing direction will be executed right away, suitable for applications of speed controls.
 - When M1616 is set to ON, the instruction of changing direction will only be executed after the last pulse is completely outputted, suitable for applications of positioning controls.
2. Adding a functionality of identifying a 12-hour time and correcting it to a 24-hour time automatically.

《Modified Functions》

1. **Bug:** If Y4 and Y6, which are in their backward output states, are controlled by DCLLM, the functions of X2 and X3 to stop inputting and outputting will fail.
Cause: The settings of Y4 and Y6 are incorrect, when working with the functions to stop inputting.
Solution: Modify the corresponding input number on the firmware.
2. **Bug:** When the setting proportion of CSFO is too low (<100%), the output errors will be increased.
Solution: Modify the operation from the integer to the floating point number to reduce the error range.
3. **Bug:** When in the process of high-speed pulse output, the system will read the current output position continuously and sometimes this may increase the position-changing error range.
Cause: 32-bit value is read by a 16-bit mode to cause timing problems.
Solution: Double confirm the output position to avoid the problems caused by reading the timing differences.
4. **Bug:** When using PLSY and PLSV to output, sometimes the output will fail in the frequency range of 63900~62920 .
Cause: 32-bit value is written by a 16-bit mode to cause order problems.
Solution: First judge and then write before changing the speed from low to high or the other way around.
5. **Bug:** When using RS to send 1 byte of data, sometimes it fails to send.
Solution: Make sure the 1 byte of data has been sent and then switching back to the receiving mode.

《Deleted Instruction》

In order to increase the firmware space, DSPA instruction is deleted.

《Official Effective Date》

The new firmware will be released on January 18, 2016. (W1604)